

PATENT ABSTRACTS OF JAPAN

①

(11)Publication number : 10-108463

(43)Date of publication of application : 24.04.1998

(51)Int.Cl.

H02M 5/27

H02M 1/088

(21)Application number : 08-252809 (71)Applicant : TOSHIBA CORP

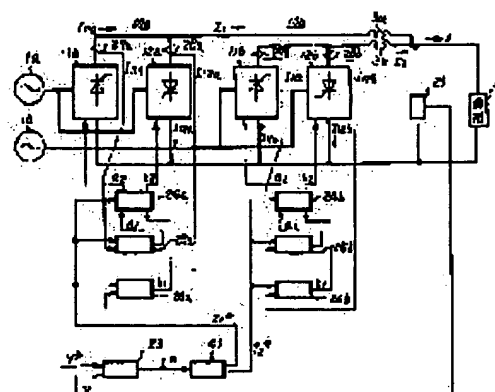
(22)Date of filing : 25.09.1996 (72)Inventor : NAKAMURA TOSHITAKA
HOSODA HIROMI

(54) POWER CONVERTER AND POWER CONVERTING SYSTEM

(57)Abstract:

PROBLEM TO BE SOLVED: To obtain a power converter, having a good operational efficiency which does not allow the generation of a current pause period, when the polarity of a current flowing into the load side is inverted and prevent an over current due to a recovery power.

SOLUTION: A function generating means 41 outputs a first and a second current standards $I1^*$, $I2^*$, depending on the current standard I^* , while a first and a second power converters 10a, 10b are individually controlled depending on $I1^*$, $I2^*$. In this case, the proportional relationship for the current standard I^* of the first and second current standards $I1^*$, $I2^*$ changes, a time difference is given to the timing for inverting polarities of the output currents $I1$, $I2$ of the first and second power converters 10a, 10b and a pause period is never generated in the current flowing into the load 5, so that the combined output current I corresponds to the current standard I^* and value of current standard I^* setting the values of the $I1^*$ and $I2^*$ to zero is different in the range, where a value of the current standard I^* is less than the predetermined value.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision
of rejection][Kind of final disposal of application
other than the examiner's decision of